In re Application of:

HATAKENAKA et al.

Application No.

09/871,978

Filed:

June 1, 2001

For:

Sir:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE COMPRISING RAM WITH COMMAND DECODE SYSTEM AND LOGIC CIRCUIT INTEGRATED INTO A SINGLE CHIP AND TESTING METHOD OF

THE RAM WITH COMMAND DECODE SYSTEM

Mai Stop Non-Fee Amendment Commissioner for Patents

Box 1450

exandria, VA 22313-1450

RECEIVED

JAN 0 2 2004

Technology Center 2100

	Small entity status	is claimed for this	application	under 37	CFR 1.27.
--	---------------------	---------------------	-------------	----------	-----------

- Petition for an extension of time for the period noted below, as well as for any additional period necessary to render the present submission timely. Please charge Deposit Account No. 12-1216 for the appropriate petition fee.
- Other: IEEE Standard Dictionary of Electrical and Electronic Terms (3 sheets)

					SMALL ENTITY		OTHER THAN A SMALL ENTITY	
TIME EXTENSION PETITION FEE			none		\$ 0.00		\$ 0.00	
	subtract time extension none fee previously paid		(\$ 0.00)		(\$ 0.00)			
CLAIM FEE	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	EXTRA CLAIMS PRESENT	RATE	Addit. Claim Fee	RATE	Addit. Claim Fee
TOTAL	18	Minus	18	=00	x 9=	\$	x 18=	\$0
INDEPENDENT	4	Minus	4	=00	x 43=	\$	x 86=	\$0
FIRST PR	ESENTATION OF MUL	TIPLE CLAIM			+ 145=	\$	+ 290=	\$
TOTAL AMOUN	IT TO BE CHARG	ED TO DEF	POSIT ACCOUNT		TOTAL	\$	TOTAL	\$0

The Commissioner is hereby authorized to charge any deficiencies in the following fees associated with this communication or credit any overpayment to Deposit Account No. 12-1216.

Any filing fees under 37 CFR 1.16 for the presentation of extra claims.

Any patent application processing fees under 37 CFR 1.17.

Respectfully, submitted,

Jeffrey A. Wyand, Reg No. 29,45

LEYDIG, VOIT & MAYER

700 Thirteenth Street N.W., Suite 300

Washington, DC 20005-3960

(202) 737-6770 (telephone) (202) 737-6776 (facsimile)

Date.

Amendment or ROA Transmittal (Revised 10/2/03)

In re Application of:

HATAKENAKA et al.

Application No.

09/871,978

Filed:

Sir:

June 1, 2001

For:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE COMPRISING RAM WITH COMMAND DECODE SYSTEM AND LOGIC CIRCUIT INTEGRATED INTO A SINGLE CHIP AND TESTING METHOD OF

THE RAM WITH COMMAND DECODE SYSTEM

Mat Stop Non-Fee Amendment Commissioner for Patents

P. (Box 1450)

xandria, VA 22313-1450

RECEIVED

JAN 0 2 2004

Technology Center 2100

	Small entity status is claimed for this application under 3	7 CFR 1.2	27.
--	---	-----------	-----

- Petition for an extension of time for the period noted below, as well as for any additional period necessary to render the present submission timely. Please charge Deposit Account No. 12-1216 for the appropriate petition fee.
- Other: IEEE Standard Dictionary of Electrical and Electronic Terms (3 sheets)
- Please charge Deposit Account No. 12-1216 in the total amount indicated below. A duplicate copy of this transmittal sheet is enclosed herewith.

			-	SMALL ENTITY		OTHER THAN A SMALL ENTITY		
TIME EXTENSION PETITION FEE	subtract time extension fee previously paid none		\$ 0.00		\$ 0.00			
,			none		(\$ 0.00)		(\$ 0.00)	
CLAIM FEE	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	EXTRA CLAIMS PRESENT	RATE	ADDIT. CLAIM FEE	RATE	ADDIT: CLAIM FEE
TOTAL	18	Minus	18	=00	x 9=	\$	x 18=	\$0
INDEPENDENT	4	Minus	4	=00	x 43=	\$	x 86=	\$0
☐ FIRST PRE	SENTATION OF MUL	TIPLE CLAIM			+ 145=	\$	+ 290=	\$
TOTAL AMOUN	T TO BE CHARG	ED TO DEF	POSIT ACCOUNT	-	TOTAL	\$	TOTAL	\$0

The Commissioner is hereby authorized to charge any deficiencies in the following fees associated with this communication or credit any overpayment to Deposit Account No. 12-1216.

Any filing fees under 37 CFR 1.16 for the presentation of extra claims.

Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted.

Jeffrey A. Wyand, Reg M LEYDIG, VOIT & MAYER

700 Thirteenth Street N.W., Suite 300

Washington, DC 20005-3960 (202) 737-6770 (telephone)

(202) 737-6770 (telephone (202) 737-6776 (facsimile)

Date: Decema 31, 2003

Amendment or ROA Transmittal (Revised 10/2/03)